

F16.

RAR INPUT CKT MASK ROM 20~ 20b-203-₹ \$ \$ \$ \$ \$ \$ ECU 48 HP ROM

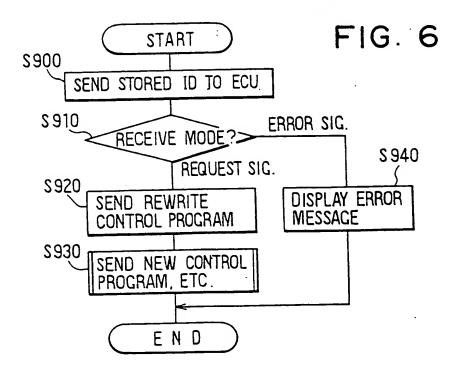
1

Inventor: TERADA et al SN 10/062,541/Sheet 2 of 12 Atty. Dkt.: 2018-505



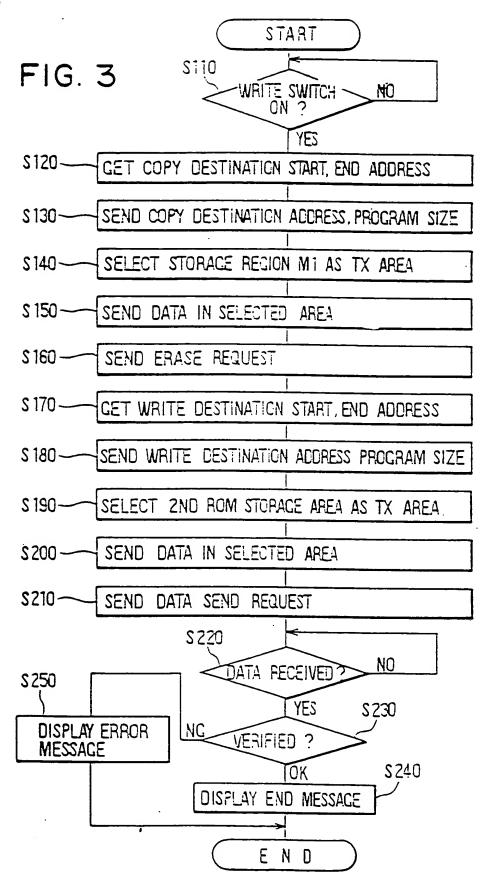
FIG. 2

R/W CONTROL R/W CONTROL PROGRAM STORAGE PROGRAM CODE AREA M1 COMMUNICATION SPEED CHANGE SECTION REWRITE INSTRUCTION SECTION R/W CONTROL PROGRAM TRANSMITTED HEAD ADDRESS R/W CONTROL PROGRAM LAST PREVIOUS TRANSMITTED HEAD ADDRESS PARAMETER STORAGE AREA R/W DATA NEXT-TO-LAST HEAD ADDRESS M2 R/W DATA LAST HEAD ADDRESS COMMUNICATION PROTOCOL INFORMATION (COMMUNICATION SPEED, FORMAT, ETC.)



Inventor: TERADA et al SN 10/062,541/Sheet 3 of 12 Atty. Dkt.: 2018-505





Inventor: TERADA et al SN 10/062,541/Sheet 4 of 12 Atty. Dkt.: 2018-505

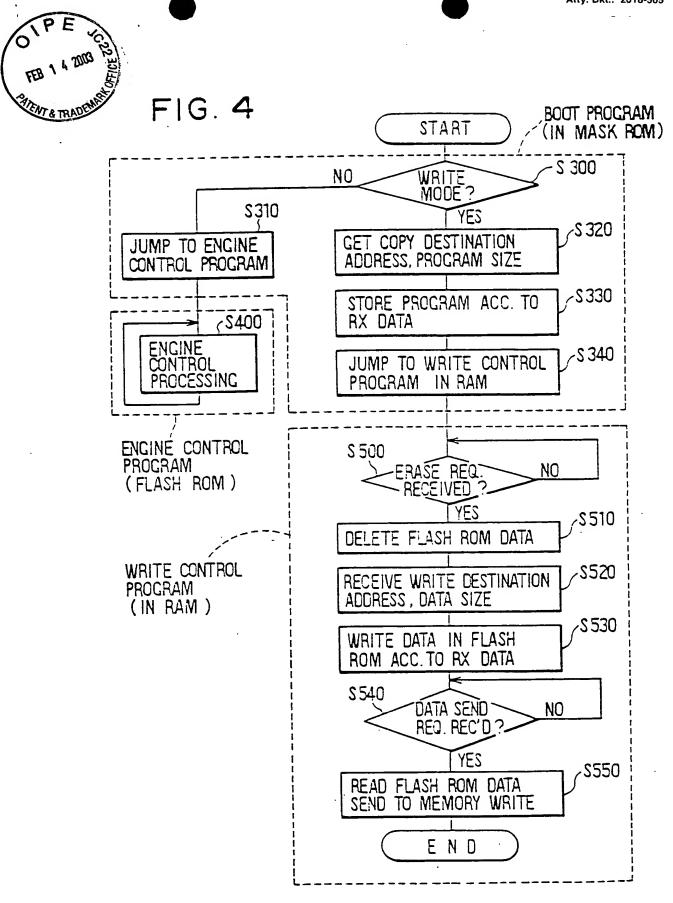




FIG. 5

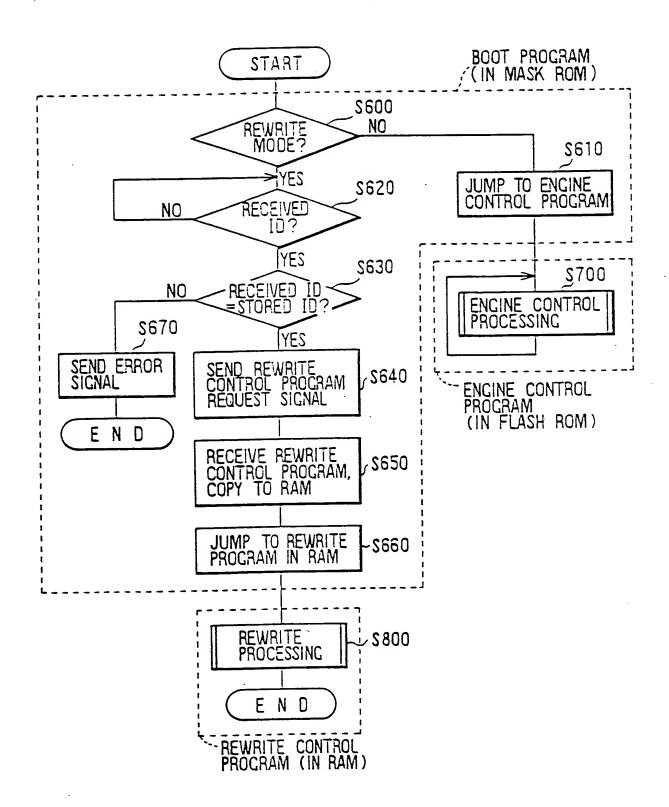
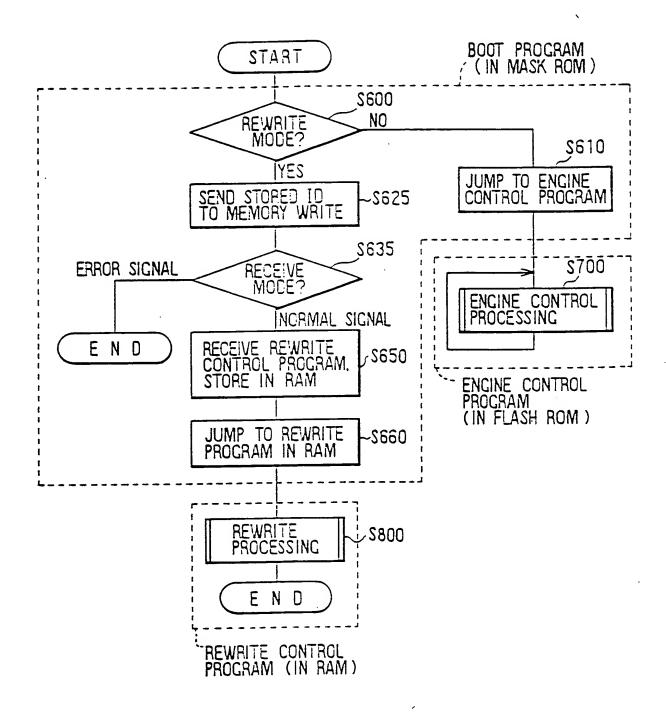
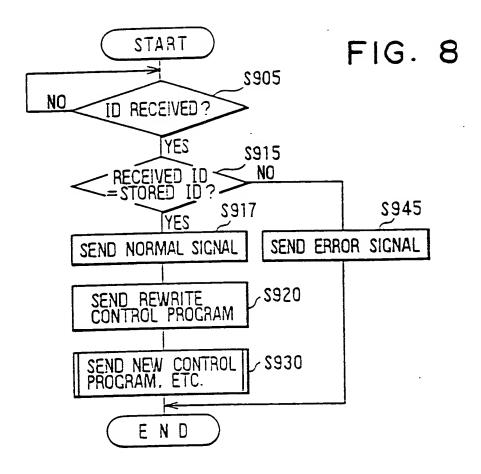


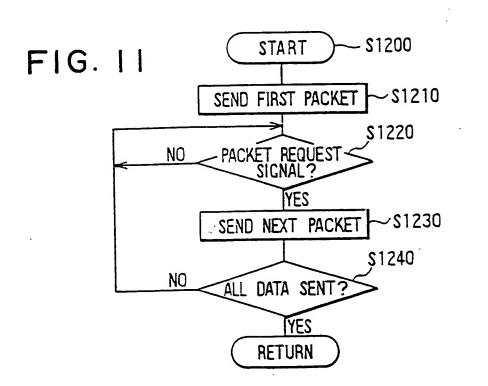


FIG. 7











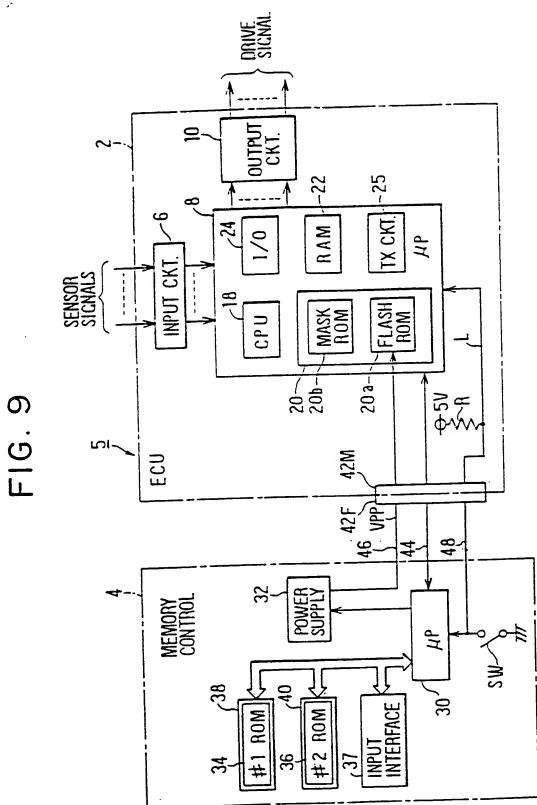
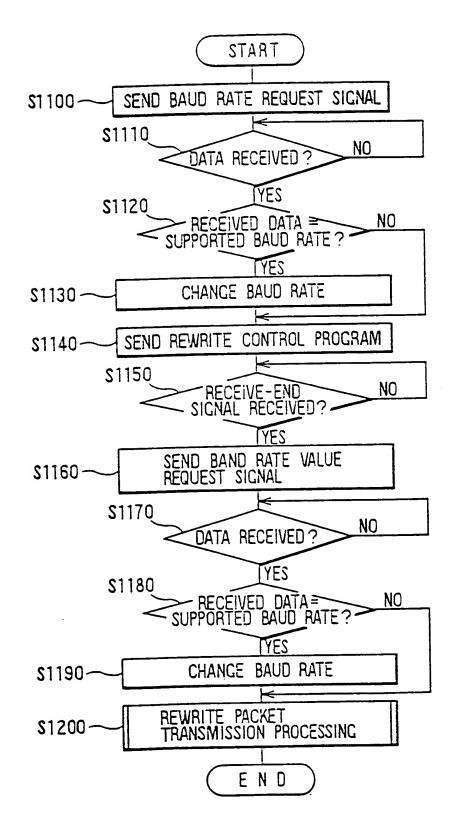
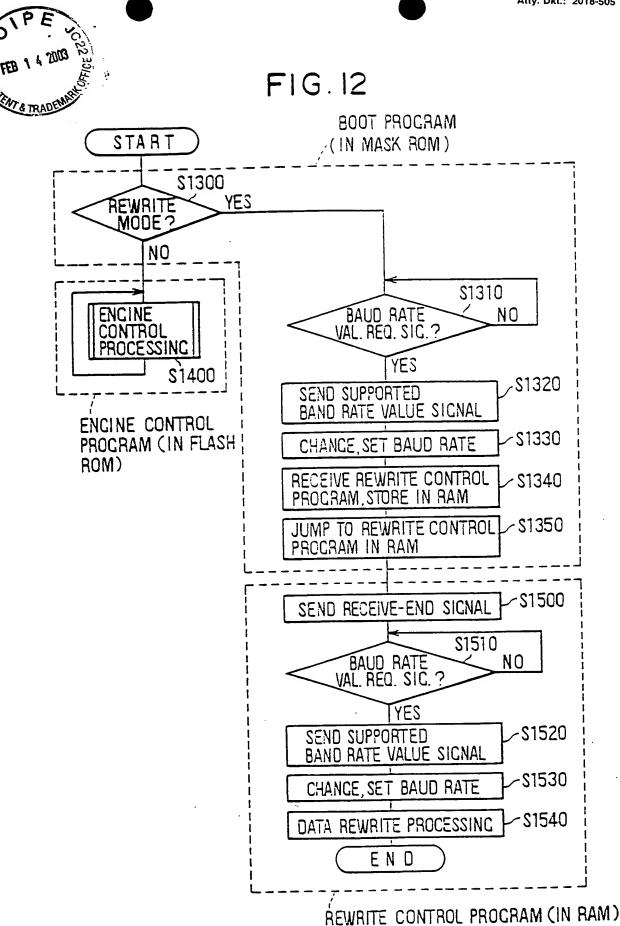




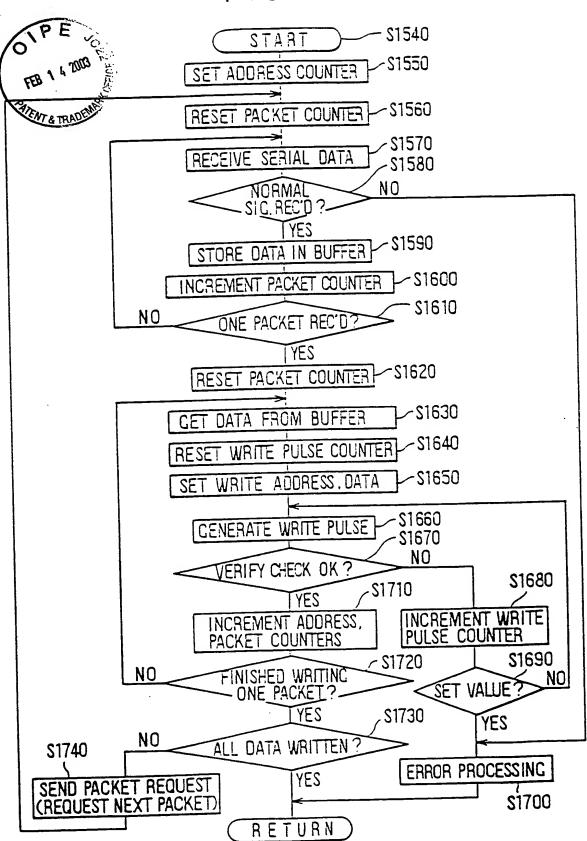
FIG. 10





Inventor: TERADA et al SN 10/062,541/Sheet 11 of 12 Atty. Dkt.: 2018-505

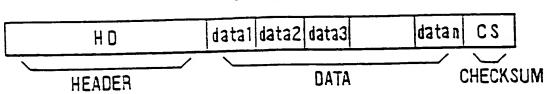
FIG. 13



Inventor: TERADA et al SN 10/062,541/Sheet 12 of 12 Atty. Dkt.: 2018-505



FIG. 14



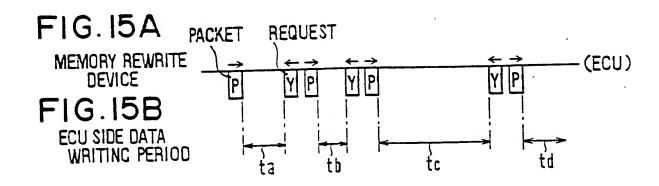


FIG. 16

